

**Dr.Mahalingam College of Engineering and Technology**

**Pollachi-642003**

**Department of Electronics and Communication Engineering**

**Publication in National/International Journal & Book Publications**

**July 2017 to June 2018**

<b>Sl. No</b>	<b>Name of Faculty</b>	<b>Title of the Paper</b>	<b>Journal Details</b>	<b>Details</b>	
1.	T.Piraisoodi M.WilljuiceIruthayar ajan <b>K.Mohaideen Abdul Kadhar</b>	Application of Single and Multi-objective Evolutionary Algorithms for optimal Nonlinear Controller Design in Boiler –Turbine System	International Journal of Fuzzy Systems, Published by Springer, Vol20, No 3, pp 803–816,March 2018, P-ISSN: 1562-2479,E-ISSN: 2199-3211. <a href="https://doi.org/10.1007/s40815-017-0382-2">DOI:10.1007/s40815-017-0382-2</a>	<b>Year of Publication</b>	March 2018,
				<b>Impact Factor</b>	2.198
				<b>Indexing</b>	Science Citation Index, SCOPUS, Google Scholar,listed in UGC
				<b>No.of Citations</b>	-
2.	T.Piraisoodi M.WilljuiceIruthayar ajan <b>K.Mohaideen Abdul Kadhar</b>	An Optimized Nonlinear Controller Design for Boiler–Turbine System Using Evolutionary Algorithms	IETE Journal of Research P-ISSN:0377-2063 E-ISSN:0974-780x PP.1-12,August 2017 <a href="https://doi.org/10.1080/03772063.2017.1360805">DOI:10.1080/03772063.2017.1360805</a>	<b>Year of Publication</b>	August 2017 (online)
				<b>Impact Factor</b>	0.909
				<b>Indexing</b>	Science Citation Index, SCOPUS, listed in UGC
				<b>No.of Citations</b>	-
3.	<b>K. N. Vijeyakumar S. Elango S. Kalaiselvi</b>	VLSI Implementation of High Speed Energy-EfficientTruncated Multiplier	Journal of Circuits, Systems, and Computers Vol. 27, No. 5,pp. 1850077-1 - 15 P- ISSN: 0218-1266 E-ISSN:1793-6454,August 2017 <a href="https://doi.org/10.1142/S0218126618500779">DOI: 10.1142/S0218126618500779</a>	<b>Year of Publication</b>	August 2017
				<b>Impact Factor</b>	0.595
				<b>Indexing</b>	<b>ESCI</b> ,SCOPUS, Google Scholar
				<b>No.of Citations</b>	-
4.	Saravanakumar <b>K.N Vijeyakumar Sakthisudhan</b>	FPGA Implementation of High Speed Hardware Efficient	International Journal of Reconfigurable and Embedded Systems (IJRES) Vol. 7, No. 1, March 2018, pp. 43~47	<b>Year of Publication</b>	March 2018
				<b>Impact Factor</b>	-

		Carry Select Adder	ISSN: 2089-4864, DOI: 10.11591/ijres.v7i1.pp43-47,March 2018	<b>Indexing</b>	
5.	S.Nagarajan, <b>K.N Vijeyakumar</b>	Efficient system of power flow in hybrid AC-DC micro-grid by implementation of bidirectional switched boost converter	Journal of Computational and Theoretical Nanoscience,Vol. 15, pp.1204–1212, 2018,March 2018	<b>Year of Publication</b>	
				<b>Impact Factor</b>	
				<b>Indexing</b>	Scopus
				<b>No.of Citations</b>	
6.	A.Senthilvel <b>K.N Vijeyakumar</b>	Efficient design and data analysis of solar power plant using advanced techniques-AFL	Journal of Computational and Theoretical Nano science, Vol. 15, pp. 1213–1220,2018,March 2018	<b>Year of Publication</b>	
				<b>Impact Factor</b>	
				<b>Indexing</b>	Scopus
				<b>Year of Publication</b>	January 2018
7.	M.AbarnaErmini J.Dhanasekar, <b>V.K.Sudha</b>	Memristor Emulator using MCP3208 AND Digital Potentiometer	ICTACT Journal on Microelectronics,Vol.3,No.4,pp.473-476 ,P-ISSN:2395-1680,January 2018  <a href="https://doi.org/10.21917/ijme.2017.0084">DOI:10.21917/ijme.2017.0084</a>	<b>Impact Factor</b>	-
				<b>Indexing</b>	-
				<b>No.of Citations</b>	-
				<b>Year of Publication</b>	March 2018
8.	Sakthi K <b>Dhinesh K</b>	PAPR Reduction Technique for universal Filter Multi Carrier	International Journal of Advanced Research in Engineering Science & Technology,E-ISSN:2393-9877,p-ISSN:2394-2444,vol 5,No.3,pp.434-438.March 2018	<b>Impact Factor</b>	-
				<b>Indexing</b>	-
				<b>No.of Citations</b>	-

**Books/Book Chapters Published 2017-2018:**

Sl. No.	Name of Faculty	Title of the Book/Book Chapters	Publisher's Name	Year of Publication
1.	<b>R.Sudhakar S.Bharathi V.Gurunathan</b>	Hybrid Intelligence for Image Analysis and Understanding, <b>Chapter 14:Analysis of Hand vein Images Using Hybrid Techniques,</b> DOI: 10.1002/9781119242963.ch14 ISBN:978-1119242925	John Wiley& Sons Ltd pp.330 to 349	August 2017
2.	<b>K.N.Vijeyakumar S Kalaiselvi B.Vinothkumar</b>	ASIC Implementation of Low Power FP-AU using Reversible Logic 53 pages ISBN : 978-6139587056	LAP Lambert Publishing Academy	April 3,2018
3.	B.Vinothkumar <b>K.N.Vijeyakumar</b> K.Saranya	Single Precision Floating Point Multiplier 63 Pages ISBN:978-3-96067-655-3	Anchor Publishers	July 2017